

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 198707US0X CONT	SERIAL NO. 09/692,371		
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Johannes KRUL, et al.			
				FILING DATE October 20, 2000	GROUP 2876		
U.S. PATENT DOCUMENTS							
EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>JH</i>	AA	5,108,819	04-28-92	HELLER, ET AL.			
<i>JH</i>	AB	5,347,144	09-13-94	GARNIER, ET AL.			
<i>JH</i>	AC	5,566,441	10-22-96	MARSH, ET AL.			
	AD						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO	
	AF	196 30 648 A1	02-05-98	GERMANY		X	
<i>JH</i>	AF	0 788 075 A2	08-05-97	EUROPE	X		
<i>JH</i>	AG	99/30432	06-17-99	WIPO	X		
<i>JH</i>	AH	96/08047	03-14-96	WIPO	X		
	AI	2049305	09-30-03	RU (equivalent to U.S. 5,108,819)	X		
	AJ						
	AK						
	AL						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>JH</i>	AM	"Currency with an Integrated Chip", IBM Technical Disclosure Bulletin, Volume 32, No. 5A, October 1989, Page 427.					
<i>JH</i>	AN	"Pentacene Thin Film Transistors and Inverter Circuits", 1997 International Electron Devices Meeting Technical Digest, December 1997, Pages 539-542.					
<i>JH</i>	AO	"New plastic circuits are flexible enough to be folded in half", Press Information, December 8, 1997.					
<i>JH</i>	AP	May, "Polymer electronics – fact or fantasy?", Physics World, March 1995, Pages 52-57.					
<i>JH</i>	AQ	The first all-polymer field effect transistor", Design Engineering, May 1995, Pages 47-48.					
<i>JH</i>	AR	Conference Papers EETimes of December 9, 1997, Internet Disclosure, Development of Philips.					
<i>JH</i>	AS	Beeuw, et al., "Polymeric Integrated Circuits and Light-Emitting Diodes", Philips Research Laboratories, 1997, Pages 331-336. <i>No month available.</i>					
<i>JH</i>	AT	Brown, et al., "Logic Gates Made from Polymer Transistors and Their Use in Ring Oscillators" Science, Volume 270, November 10, 1995, Pages 972-974.					
<i>JH</i>	AI	Bonnaud, et al., "Evaluation des performances de circuits logiques réalisés à partir de transistors MIS organiques", L'Onde Electrique – Juillet-Août, 1994, Volume 74, NO. 4, Pages 18-24.					
<i>JH</i>	AV	Communication of a Notice of Opposition Against the Equivalent European Patent 1073993, Filed By De La Rue International Limited. <i>9/2003</i>					
<i>JH</i>	AW	Communication of a Notice of Opposition Against the Equivalent European Patent 1073993, Filed By Giesecke & Devrient GmbH. <i>9/2003</i>					
	AX						
					<input type="checkbox"/> Additional References sheet(s) attached		
Examiner <i>Geno J. Finneran</i>					Date Considered <i>2/24/2004</i>		

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.